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**UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **Hiroshi HASHIMOTO et al.**

Art Unit: **2814**

Application Number: **10/083,533**

Examiner: **Thao X. Le**

Filed: **February 27, 2002**

Confirmation Number: **6400**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A  
SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD  
CHARACTERISTIC**

Attorney Docket Number: **020244**

Customer Number: **38834**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

March 26, 2008

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated October 26, 2007 for two months from January 26, 2008 to March 26, 2008.

Attached please find a check in the amount of \$460.00 for the cost of the extension of time. If any additional fees are due in connection with this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,

**WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP**

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